

Brochure for ECE__GATE Coaching

GATE COACHING 2019-20

Date: - 30.11.2019 to 20.01.2020 (5.30-7.30 PM IST)

Organised by :

Department of Electronics and communication Engineering

Resource Person

Mr Rajesh kumar Misra(ECE) Mrs Jeevaratam(ECE) Mr Sandipan Pine(ECE) **Coordinators :**

Mr. Rajesh Mishra Assistant Professor Training & Placement

Dept. Of Electronics & communicaton Engineering

CONVENOR





9. Attendance for ECE__GATE Coaching

| Attendance for ECE GATE Coaching | | | | | |
|----------------------------------|--------------------------------|------------------|----------|----------|----------|
| | | | Date | | |
| SI.No. | Name of Students | Students Reg No. | 30th Nov | 15th Dec | 21st Dec |
| 1 | SHANTILATA PANIGRAHI | 160101130001 | Р | Р | Р |
| 2 | POTNURU JAGADISH | 160101130002 | Р | Р | Р |
| 3 | ARCHANA RANI MISHAL | 160101130003 | Р | Р | Р |
| 4 | PAILU DHILEEP KUMAR | 160101130004 | Р | Р | Р |
| 5 | BATTINI HARISH | 160101130005 | Р | Р | Р |
| 6 | POLAKI SRAVAN | 160101130006 | Р | Р | Р |
| 7 | DINESH PATNAIK | 160101130007 | Р | Р | Р |
| 8 | SARISABADRA PAWAN KUMAR | 160101130008 | Р | Р | Р |
| 9 | SIRIPURAM VIKAS | 160101130009 | Р | Р | Р |
| 10 | LABHALA CHANDRIKA | 160101130010 | Р | Р | Р |
| 11 | TALASALA SIRI PAVANA TIRUTHANI | 160101130011 | Р | Р | Р |
| 12 | SYED MEERAJ | 160101130012 | Р | Р | Р |
| 13 | SWARAJ PANDA | 160101130013 | Р | Р | Р |
| 14 | AMAN KUMAR PADHY | 160101130014 | Р | Р | Р |
| 15 | DURGASI PRATAP | 160101130015 | Р | Р | Р |
| 16 | SIMHADRIDHANUNJAYA ACHARY | 160101130016 | Р | Р | Р |
| 17 | TAMADA ANITHA | 160101130017 | Р | Р | Р |
| 18 | GORAPALLIPUSHPA LATHA | 160101130018 | Р | Р | Р |
| 19 | KASINA REVATHI | 160101130019 | Р | Р | Р |
| 20 | PADUMPURAM VISWA TEJA | 160101130020 | Р | Р | Р |

CUTM

CONVENOR



GATE COACHING 2019-2020

(B.TECH ECE)

Name of the facilitator – Mr. Satayanarayan Padhy Mr. Sandipan Pine & Mrs.

N.Jeevaratam

Date: - 30 Nov 2019 to 20 Jan 2020 Participants:-20

Department Of Electronics and Communication Engineering conducted GATE classes on different subjects like Analog circuit,Digital circuit,Anteena Design, Microprocessor and VLSI design by our concern faculties. A total number of 20 students attended. The facilitators taught the numerical problems asked in the GATE . and also spoke on how to get prepared for answering GATE questions in less time.

Our career coordinator Mr. Rajesh Kumar Misra, Assistant Professor, SOET, presented the vote of thanks by expressing his gratitude to the chief speakers .He also thanked all the students and faculty present in the talk

CONVENOR