

ation of Coconut Fibre. Thesis Master's Degree

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Chapter 7

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MULTILEVEL INVERTER TOPOLOGIES

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7.1. multilevel inverter topologies

Multilevel inverter (MLI) comprises a pattern of power electronics elements along with voltage sources and capacitors, which gives a stepped waveform as an output result. The combination of the power electronic switches allows the addition of the voltages across the capacitors to provide an output voltage of different levels. In a two level inverter the output voltage will have two levels or value, that is high or low. Similarly in a three level inverter the output voltage is of three values or levels and in a nine level inverter the output voltages of nine values. The elements of multilevel inverters are organized in such a way that the inverters can provide the required type of voltage waveform as an output. The harmonics will come down